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We hereby revoke all previous powers of attorney given in the US applications/patents identified in the attached USPTO Patent Assignment Details, recorded at the United States Patent & Trademark Office on July 13, 2009 at the Reel/Frame 022939/0399 and appoint the practitioners associated with the Customer Number 25570 to act on our behalf for each of the identified US applications and/or patents recorded by U.S. Patent and Trademark Office on July 13, 2009 in Reel/Frame 022939/0399.

Please change the correspondence address for each of the applications/patents identified in the attached USPTO Patent Assignment Details, recorded at the United States Patent & Trademark Office on July 13, 2009 at the Reel/Frame 022939/0399 for all purposes, including the fee address, to the address associated with Customer Number 25570.

Patent Owner, Microelectronic Technology, Inc., is the Assignee of record of the entire interest. A Statement under 37 C.F.R. 3.73(b) is enclosed. The undersigned is authorized to act on behalf of the assignee.

  
Signature

7-16-2009  
Date

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Mr. Timothy HUANG  
Authorized representative of  
MICROELECTRONICS TECHNOLOGY, INC.

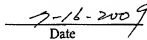
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STATEMENT UNDER 37 CFR 3.73(b)

Patent Owner, Microelectronic Technology, Inc., a corporation, states that it is the assignee of the entire right, title, and interest in the U.S. patent applications/patents identified in the attached USPTO Patent Assignment Details, recorded at the United States Patent & Trademark Office on July 13, 2009 at the Reel/Frame 022939/0399.

The undersigned is authorized to act on behalf of the assignee.

  
Signature

  
Date

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Mr. Timothy HUANG  
Authorized representative of  
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#### Patent Assignment Details

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Reel/Frame: 022939/0399

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Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

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1	Patent #: <u>4941153</u> Title: HIGH-SPEED DIGITAL DATA COMMUNICATION SYSTEM	Issue Dt: 07/10/1990	Application #: 07089281	Filing Dt: 08/25/1987
2	Patent #: <u>4975931</u> Title: HIGH SPEED PROGRAMMABLE DIVIDER	Issue Dt: 12/04/1990	Application #: 07286435	Filing Dt: 12/19/1988
3	Patent #: <u>5058107</u> Title: EFFICIENT DIGITAL FREQUENCY DIVISION MULTIPLEXED SIGNAL RECEIVER	Issue Dt: 10/15/1991	Application #: 07293894	Filing Dt: 01/05/1989
4	Patent #: <u>5130578</u> Title: EFFICIENT HIGH SPEED N-WORD COMPARATOR	Issue Dt: 07/14/1992	Application #: 07444454	Filing Dt: 11/30/1989
5	Patent #: <u>5128534</u> Title: HIGH CHARGE CAPACITY FOCAL PLANE ARRAY READOUT CELL	Issue Dt: 07/07/1992	Application #: 07554238	Filing Dt: 07/17/1990
6	Patent #: <u>5271038</u> Title: DISTORTION SUPPRESSION USING THRESHOLDING TECHNIQUES	Issue Dt: 12/14/1993	Application #: 07580710	Filing Dt: 09/10/1990
7	Patent #: <u>5303161</u> Title: TECHNOLOGY INDEPENDENT INTEGRATED CIRCUIT MASK ARTWORK GENERATOR	Issue Dt: 04/12/1994	Application #: 07624958	Filing Dt: 12/10/1990
8	Patent #: <u>5164959</u> Title: DIGITAL EQUALIZATION METHOD AND APPARATUS	Issue Dt: 11/17/1992	Application #: 07643969	Filing Dt: 01/22/1991
9	Patent #: <u>5267272</u> Title: A RECEIVER AUTOMATIC GAIN CONTROL (AGC)	Issue Dt: 11/30/1993	Application #: 07655684	Filing Dt: 02/14/1991
10	Patent #: <u>5136205</u> Title: MICROELECTRONIC FIELD EMISSION DEVICE WITH AIR BRIDGE ANODE	Issue Dt: 08/04/1992	Application #: 07675590	Filing Dt: 03/26/1991
11	Patent #: <u>5128674</u> Title: TWO QUADRANTS HIGH SPEED MULTIPLYING DAC	Issue Dt: 07/07/1992	Application #: 07676635	Filing Dt: 03/28/1991
12	Patent #: <u>5205647</u> Title: LOW COST AGC FUNCTION FOR MULTIPLE APPROXIMATION A/D CONVERTERS	Issue Dt: 04/27/1993	Application #: 07722763	Filing Dt: 06/27/1991

13	<b>Patent #:</b> <u>5251218</u> <b>Title:</b> EFFICIENT DIGITAL FREQUENCY DIVISION MULTIPLEXED SIGNAL RECEIVER	<b>Issue Dt:</b> 10/05/1993	<b>Application #:</b> 07739593	<b>Filing Dt:</b> 07/31/1991
14	<b>Patent #:</b> <u>5220552</u> <b>Title:</b> MULTIPLE USE DIGITAL TRANSMITTER/TRANSCIVER WITH TIME MULTIPLEXING	<b>Issue Dt:</b> 06/15/1993	<b>Application #:</b> 07765157	<b>Filing Dt:</b> 09/23/1991
15	<b>Patent #:</b> <u>5304951</u> <b>Title:</b> DIVIDER SYNCHRONIZATION CIRCUIT FOR PHASE-LOCKED LOOP FREQUENCY SYNTHESIZER	<b>Issue Dt:</b> 04/19/1994	<b>Application #:</b> 07829183	<b>Filing Dt:</b> 01/31/1992
16	<b>Patent #:</b> <u>5313113</u> <b>Title:</b> SAMPLE AND HOLD CIRCUIT WITH FULL SIGNAL MODULATION COMPENSATION USING BIPOLAR TRANSISTORS OF SINGLE CONDUCTIVITY TYPE	<b>Issue Dt:</b> 05/17/1994	<b>Application #:</b> 07870369	<b>Filing Dt:</b> 04/17/1992
17	<b>Patent #:</b> <u>5250911</u> <b>Title:</b> SINGLE-ENDED AND DIFFERENTIAL TRANSISTOR AMPLIFIER CIRCUITS WITH FULL SIGNAL MODULATION COMPENSATION TECHNIQUES WHICH ARE TECHNOLOGY INDEPENDENT	<b>Issue Dt:</b> 10/05/1993	<b>Application #:</b> 07871861	<b>Filing Dt:</b> 04/20/1992
18	<b>Patent #:</b> <u>5315169</u> <b>Title:</b> POWER-EFFICIENT SAMPLE AND HOLD CIRCUIT USING BIPOLAR TRANSISTORS OF SINGLE CONDUCTIVITY TYPE	<b>Issue Dt:</b> 05/24/1994	<b>Application #:</b> 07894980	<b>Filing Dt:</b> 06/08/1992
19	<b>Patent #:</b> <u>5278837</u> <b>Title:</b> MULTIPLE USER DIGITAL RECEIVER APPARATUS AND METHOD WITH COMBINED MULTIPLE FREQUENCY CHANNELS	<b>Issue Dt:</b> 01/11/1994	<b>Application #:</b> 07905965	<b>Filing Dt:</b> 06/29/1992
20	<b>Patent #:</b> <u>5350952</u> <b>Title:</b> SAMPLE AND HOLD CIRCUIT WITH PUSH-PULL OUTPUT CHARGING CURRENT	<b>Issue Dt:</b> 09/27/1994	<b>Application #:</b> 07909286	<b>Filing Dt:</b> 07/06/1992
21	<b>Patent #:</b> <u>5315231</u> <b>Title:</b> SYMMETRICAL BIPOLAR BIAS CURRENT SOURCE WITH HIGH POWER SUPPLY REJECTION RATIO (PSRR)	<b>Issue Dt:</b> 05/24/1994	<b>Application #:</b> 07976760	<b>Filing Dt:</b> 11/16/1992
22	<b>Patent #:</b> <u>5621730</u> <b>Title:</b> MULTIPLE USER DIGITAL RECEIVER APPARATUS AND METHOD WITH TIME DIVISION MULTIPLEXING	<b>Issue Dt:</b> 04/15/1997	<b>Application #:</b> 07986180	<b>Filing Dt:</b> 12/07/1992
23	<b>Patent #:</b> <u>5483150</u> <b>Title:</b> TRANSISTOR CURRENT SWITCH ARRAY FOR DIGITAL-TO-ANALOG CONVERTER (DAC) INCLUDING BIAS CURRENT COMPENSATION FOR INDIVIDUAL TRANSISTOR CURRENT GAIN AND THERMALLY INDUCED BASE-EMITTER VOLTAGE DROP VARIATION	<b>Issue Dt:</b> 01/09/1996	<b>Application #:</b> 08017200	<b>Filing Dt:</b> 02/05/1993
24	<b>Patent #:</b> <u>5343163</u> <b>Title:</b> SINGLE-ENDED AND DIFFERENTIAL TRANSISTOR AMPLIFIER CIRCUITS WITH FULL SIGNAL MODULATION COMPENSATION TECHNIQUES WHICH ARE TECHNOLOGY INDEPENDENT	<b>Issue Dt:</b> 08/30/1994	<b>Application #:</b> 08080269	<b>Filing Dt:</b> 06/21/1993
25	<b>Patent #:</b> <u>5428305</u> <b>Title:</b> DIFFERENTIAL LOGIC LEVEL TRANSLATOR CIRCUIT WITH DUAL OUTPUT LOGIC LEVELS SELECTABLE BY POWER CONNECTOR OPTIONS	<b>Issue Dt:</b> 06/27/1995	<b>Application #:</b> 08129939	<b>Filing Dt:</b> 09/30/1993
26	<b>Patent #:</b> <u>5410274</u> <b>Title:</b> SINGLE-ENDED AND DIFFERENTIAL AMPLIFIERS WITH HIGH FEEDBACK INPUT IMPEDANCE AND LOW DISTORTION	<b>Issue Dt:</b> 04/25/1995	<b>Application #:</b> 08210269	<b>Filing Dt:</b> 03/17/1994
27	<b>Patent #:</b> <u>5592181</u> <b>Title:</b> VEHICLE POSITION TRACKING TECHNIQUE	<b>Issue Dt:</b> 01/07/1997	<b>Application #:</b> 08443519	<b>Filing Dt:</b> 05/18/1995
28	<b>Patent #:</b> <u>5572220</u> <b>Title:</b> TECHNIQUE TO DETECT ANGLE OF ARRIVAL WITH LOW AMBIGUITY	<b>Issue Dt:</b> 11/05/1996	<b>Application #:</b> 08443537	<b>Filing Dt:</b> 05/18/1995
29	<b>Patent #:</b> <u>5581213</u> <b>Title:</b> VARIABLE GAIN AMPLIFIER CIRCUIT	<b>Issue Dt:</b> 12/03/1996	<b>Application #:</b> 08479284	<b>Filing Dt:</b> 06/07/1995
30	<b>Patent #:</b> <u>5848160</u> <b>Title:</b> DIGITAL SYNTHESIZED WIDEBAND NOISE-LIKE WAVEFORM	<b>Issue Dt:</b> 12/08/1998	<b>Application #:</b> 08603673	<b>Filing Dt:</b> 02/20/1996

31	<b>Patent #:</b> <u>5729576</u> <b>Title:</b> INTERFERENCE CANCELING RECEIVER	<b>Issue Dt:</b> 03/17/1998	<b>Application #:</b> 08641452	<b>Filing Dt:</b> 04/30/1996
32	<b>Patent #:</b> <u>5684435</u> <b>Title:</b> ANALOG WAVEFORM COMMUNICATIONS REDUCED INSTRUCTION SET PROCESSOR	<b>Issue Dt:</b> 11/04/1997	<b>Application #:</b> 08653930	<b>Filing Dt:</b> 05/22/1996
33	<b>Patent #:</b> <u>5856760</u> <b>Title:</b> OVERDRIVE PROTECTION CLAMP SCHEME FOR FEEDBACK AMPLIFIERS	<b>Issue Dt:</b> 01/05/1999	<b>Application #:</b> 08745070	<b>Filing Dt:</b> 11/07/1996
34	<b>Patent #:</b> <u>5774318</u> <b>Title:</b> I.C. POWER SUPPLY TERMINAL PROTECTION CLAMP	<b>Issue Dt:</b> 06/30/1998	<b>Application #:</b> 08753647	<b>Filing Dt:</b> 11/27/1996
35	<b>Patent #:</b> <u>5960040</u> <b>Title:</b> COMMUNICATION SIGNAL PROCESSORS AND METHODS	<b>Issue Dt:</b> 09/28/1999	<b>Application #:</b> 08761103	<b>Filing Dt:</b> 12/05/1996
36	<b>Patent #:</b> <u>5870402</u> <b>Title:</b> MULTIPLE USER DIGITAL RECEIVER APPARATUS AND METHOD WITH TIME DIVISION MULTIPLEXING	<b>Issue Dt:</b> 02/09/1999	<b>Application #:</b> 08764808	<b>Filing Dt:</b> 12/12/1996
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38	<b>Patent #:</b> <u>5859558</u> <b>Title:</b> LOW VOLTAGE ANALOG FRONT END	<b>Issue Dt:</b> 01/12/1999	<b>Application #:</b> 08827855	<b>Filing Dt:</b> 04/11/1997
39	<b>Patent #:</b> <u>5859569</u> <b>Title:</b> CURRENT FEEDBACK DIFFERENTIAL AMPLIFIER CLAMP	<b>Issue Dt:</b> 01/12/1999	<b>Application #:</b> 08843200	<b>Filing Dt:</b> 04/14/1997
40	<b>Patent #:</b> <u>6040731</u> <b>Title:</b> DIFFERENTIAL PAIR GAIN CONTROL STAGE	<b>Issue Dt:</b> 03/21/2000	<b>Application #:</b> 08848930	<b>Filing Dt:</b> 05/01/1997
41	<b>Patent #:</b> <u>5859559</u> <b>Title:</b> MIXER STRUCTURES WITH ENHANCED CONVERSION GAIN REDUCED SPURIOUS SIGNALS	<b>Issue Dt:</b> 01/12/1999	<b>Application #:</b> 08903657	<b>Filing Dt:</b> 07/31/1997
42	<b>Patent #:</b> <u>6118811</u> <b>Title:</b> SELF-CALIBRATING, SELF-CORRECTING TRANSCEIVERS AND METHODS	<b>Issue Dt:</b> 09/12/2000	<b>Application #:</b> 08903807	<b>Filing Dt:</b> 07/31/1997
43	<b>Patent #:</b> <u>5990815</u> <b>Title:</b> MONOLITHIC CIRCUIT AND METHOD FOR ADDING A RANDOMIZED DITHER SIGNAL TO THE FINE QUANTIZER ELEMENT OF A SUBRANGING ANALOG-TO DIGITAL CONVERTER (ADC)	<b>Issue Dt:</b> 11/23/1999	<b>Application #:</b> 08941457	<b>Filing Dt:</b> 09/30/1997
44	<b>Patent #:</b> <u>5995535</u> <b>Title:</b> RAPID TIME AND FREQUENCY ACQUISITION OF SPREAD SPECTRUM WAVEFORMS VIA AMBIGUITY TRANSFORM	<b>Issue Dt:</b> 11/30/1999	<b>Application #:</b> 08965251	<b>Filing Dt:</b> 11/06/1997
45	<b>Patent #:</b> <u>5926123</u> <b>Title:</b> SELF CALIBRATION CIRCUITRY AND ALGORITHM FOR MULTIPASS ANALOG TO DIGITAL CONVERTER INTERSTAGE GAIN CORRECTION	<b>Issue Dt:</b> 07/20/1999	<b>Application #:</b> 08986942	<b>Filing Dt:</b> 12/08/1997
46	<b>Patent #:</b> <u>5973631</u> <b>Title:</b> TEST CIRCUIT AND METHOD OF TRIMMING A UNARY DIGITAL-TO- ANALOG CONVERTER (DZC) IN A SUBRANGING ANALOG-TO-DIGITAL CONVERTER (ADC)	<b>Issue Dt:</b> 10/26/1999	<b>Application #:</b> 09009612	<b>Filing Dt:</b> 01/20/1998
47	<b>Patent #:</b> <u>5963094</u> <b>Title:</b> MONOLITHIC CLASS AB SHUNT-SHUNT FEEDBACK CMOS LOW NOISE AMPLIFIER HAVING SELF BIAS	<b>Issue Dt:</b> 10/05/1999	<b>Application #:</b> 09027241	<b>Filing Dt:</b> 02/20/1998
48	<b>Patent #:</b> <u>6157224</u> <b>Title:</b> HIGH SPEED PIN DRIVER INTEGRATED CIRCUIT ARCHITECTURE FOR COMMERCIAL AUTOMATIC TEST EQUIPMENT APPLICATIONS	<b>Issue Dt:</b> 12/05/2000	<b>Application #:</b> 09219759	<b>Filing Dt:</b> 12/23/1998

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49	<b>Publication #:</b> <u>US20020055347</u>	<b>Pub Dt:</b> 05/09/2002		
	<b>Title:</b> WIDEBAND IF IMAGE REJECTING RECEIVER			
50	<b>Patent #:</b> <u>6891424</u>	<b>Issue Dt:</b> 05/10/2005	<b>Application #:</b> 09408114	<b>Filing Dt:</b> 09/29/1999
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51	<b>Patent #:</b> <u>6552343</u>	<b>Issue Dt:</b> 04/22/2003	<b>Application #:</b> 09574123	<b>Filing Dt:</b> 05/18/2000
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52	<b>Patent #:</b> <u>6931083</u>	<b>Issue Dt:</b> 08/16/2005	<b>Application #:</b> 09579596	<b>Filing Dt:</b> 05/26/2000
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53	<b>Patent #:</b> <u>6535062</u>	<b>Issue Dt:</b> 03/18/2003	<b>Application #:</b> 09607223	<b>Filing Dt:</b> 06/30/2000
	<b>Title:</b> Low noise, low distortion, complementary IF amplifier			
54	<b>Patent #:</b> <u>6693980</u>	<b>Issue Dt:</b> 02/17/2004	<b>Application #:</b> 09664298	<b>Filing Dt:</b> 09/18/2000
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55	<b>Patent #:</b> <u>6580383</u>	<b>Issue Dt:</b> 06/17/2003	<b>Application #:</b> 09703646	<b>Filing Dt:</b> 11/01/2000
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56	<b>Patent #:</b> <u>6975189</u>	<b>Issue Dt:</b> 12/13/2005	<b>Application #:</b> 09705134	<b>Filing Dt:</b> 11/02/2000
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57	<b>Patent #:</b> <u>6400229</u>	<b>Issue Dt:</b> 06/04/2002	<b>Application #:</b> 09790796	<b>Filing Dt:</b> 02/22/2001
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58	<b>Patent #:</b> <u>6717450</u>	<b>Issue Dt:</b> 04/06/2004	<b>Application #:</b> 10144175	<b>Filing Dt:</b> 05/13/2002
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59	<b>Publication #:</b> <u>US20030210737</u>	<b>Pub Dt:</b> 11/13/2003		
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60	<b>Publication #:</b> <u>US20050030216</u>	<b>Pub Dt:</b> 02/10/2005		
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62	<b>Publication #:</b> <u>US20040257125</u>	<b>Pub Dt:</b> 12/23/2004		
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63	<b>Publication #:</b> <u>US20040257058</u>	<b>Pub Dt:</b> 12/23/2004		
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64	<b>Publication #:</b> <u>US20050035892</u>	<b>Pub Dt:</b> 02/17/2005		
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65	<b>Publication #:</b> <u>US20050035821</u>	<b>Pub Dt:</b> 02/17/2005		
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66	<b>Publication #:</b> <u>US20050035790</u>	<b>Pub Dt:</b> 02/17/2005		
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	<b>Patent #:</b> NONE	<b>Issue Dt:</b>	<b>Application #:</b> 10740334	<b>Filing Dt:</b> 12/18/2003
67	<b>Publication #:</b> <u>US20050035788</u>	<b>Pub Dt:</b> 02/17/2005		
	<b>Title:</b> Clamped comparator			
	<b>Patent #:</b> NONE	<b>Issue Dt:</b>	<b>Application #:</b> 10847433	<b>Filing Dt:</b> 05/17/2004
68	<b>Publication #:</b> <u>US20050038846</u>	<b>Pub Dt:</b> 02/17/2005		
	<b>Title:</b> Substraction circuit with a dummy digital to analog converter			
	<b>Patent #:</b> <u>7088148</u>	<b>Issue Dt:</b> 08/08/2006	<b>Application #:</b> 10863561	<b>Filing Dt:</b> 06/08/2004
69	<b>Publication #:</b> <u>US20050035791</u>	<b>Pub Dt:</b> 02/17/2005		
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	<b>Patent #:</b> <u>7098700</u>	<b>Issue Dt:</b> 08/29/2006	<b>Application #:</b> 10886850	<b>Filing Dt:</b> 07/08/2004
70	<b>Publication #:</b> <u>US20050127955</u>	<b>Pub Dt:</b> 06/16/2005		
	<b>Title:</b> LOW POWER OUTPUT DRIVER			
	<b>Patent #:</b> <u>7154421</u>	<b>Issue Dt:</b> 12/26/2006	<b>Application #:</b> 10890443	<b>Filing Dt:</b> 07/12/2004
71	<b>Publication #:</b> <u>US20050128118</u>	<b>Pub Dt:</b> 06/16/2005		
	<b>Title:</b> DNL/INL TRIM TECHNIQUES FOR COMPARATOR BASED ANALOG TO DIGITAL CONVERTERS			
	<b>Patent #:</b> NONE	<b>Issue Dt:</b>	<b>Application #:</b> 10967963	<b>Filing Dt:</b> 10/19/2004
72	<b>Publication #:</b> <u>US20050083223</u>	<b>Pub Dt:</b> 04/21/2005		
	<b>Title:</b> Resolution enhanced folding amplifier			
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73	<b>Publication #:</b> <u>US20050270107</u>	<b>Pub Dt:</b> 12/08/2005		
	<b>Title:</b> LOW DISTORTION AMPLIFIER			
	<b>Patent #:</b> NONE	<b>Issue Dt:</b>	<b>Application #:</b> 11150445	<b>Filing Dt:</b> 06/09/2005
74	<b>Publication #:</b> <u>US20060078065</u>	<b>Pub Dt:</b> 04/13/2006		
	<b>Title:</b> DIGITAL PRE-DISTORTION TECHNIQUE USING NONLINEAR FILTERS			
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